



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
(Case No. RA001C10)

In the Application of:

FARMWALD ET AL.

Serial No: 09/514,872

Filed: FEBRUARY 28, 2000

Title: METHOD AND APPARATUS FOR
CONTROLLING A SYNCHRONOUS
MEMORY DEVICE

Group

Art Unit: 2781

Before

Examiner: G. Auv

RECEIVED

DEC 07 2000

OFFICE OF PETITIONS

Assistant Commissioner for Patents
Washington, DC 20231

AMENDMENT

Dear Sir:

Kindly amend the application as follows:

IN THE CLAIMS:

Kindly Amend the following claims:

152. (Amended) The method of claim 151 wherein the write request includes an operation code[, the first portion of data and the second portion of data are included in a packet].

153. (Amended) The method of claim 152 wherein the memory device receives the operation code synchronously with respect to an edge transition of the external clock signal [write request, the first